

Appl. No. 10/032,761  
Amdr. dated August 11, 2003  
Reply to Office Action of April 11, 2003

PATENT

REMARKS/ARGUMENTS

Claims 6-9, 12, 15-19 are pending. Claim 1 has been amended. Claims 1-5, 10, 11, 13, and 14 have been canceled. New claims 16-19 have been added.

Claims 1-5, 10, 11, 13, and 14 were rejected under 35 U.S.C. § 102(a) as being anticipated by Person. Applicant respectfully traverse the rejection. The above claims have been canceled.

Claims 6-9, 12, and 15 were rejected as being anticipated by Takeuchi. Applicants respectfully traverse the rejection. The claimed embodiment is directed to providing an integrated device having a driver circuit and a load-current-output circuit. In claim 6, two buffer circuits and a diode bridge are coupled to provide a direct current to a device under test.

Takeuchi, on the other hand, is directed to control rise and fall time of a driver circuit (see Abstract). It discloses a circuit configuration that provides an output waveform that has desirable and stable waveform. For this purposes, the output of a diode bridge 6i is coupled to a device under test (DUT) via a condenser (Fig. 5), thereby providing alternating current to the DUT.

Therefore, Takeuchi does not disclose "... a diode bridge terminal having a pair of a third and fourth output terminals each connected to said first and said second output terminals in the form of direct current, wherein said fourth output terminal supplies a current as a driver when outputting a test waveform to a device under test (DUT)...," as recited in claim 6. Claim 6 and its dependent claims are allowable.

CONCLUSION

In view of the foregoing, Applicants believe all claims now pending in this Application are in condition for allowance. The issuance of a formal Notice of Allowance at an early date is respectfully requested.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 650-326-2400.

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Respectfully submitted,



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